

## AMENDMENT TRANSMITTAL LETTER (Small Entity)

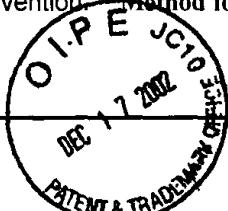
Applicant(s): Grutzdiek et al.

Docket No.

1477.011

Serial No.  
09/806,224Filing Date  
06/11/2001Examiner  
Suk San FoongGroup Art Unit  
2823

Invention: Method for Producing Transistors

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

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Transmitted herewith is an amendment in the above-identified application.

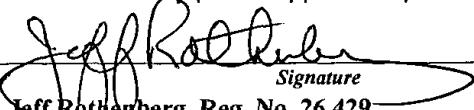
- Small Entity status of this application has been established under 37 CFR 1.27 by a verified statement previously submitted.
- A verified statement to establish Small Entity status under 37 FR 1.27 is enclosed.

The fee has been calculated and is transmitted as shown below.

## CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	30 -	27 =	3	x \$9.00	\$27.00
INDEP. CLAIMS	1 -	3 =	0	x \$42.00	\$0.00
Multiple Dependent Claims (check if applicable)	<input type="checkbox"/>				\$0.00
					TOTAL ADDITIONAL FEE FOR THIS AMENDMENT
					\$27.00

- No additional fee is required for amendment.
- Please charge Deposit Account No. in the amount of  
A duplicate copy of this sheet is enclosed.
- A check in the amount of \$27.00 to cover the filing fee is enclosed.
- The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 08-1935  
A duplicate copy of this sheet is enclosed.
- Any additional filing fees required under 37 C.F.R. 1.16.
- Any patent application processing fees under 37 CFR 1.17.

  
Signature  
Jeff Rothenberg, Reg. No. 26,429

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Dated: Dec (2) 2002

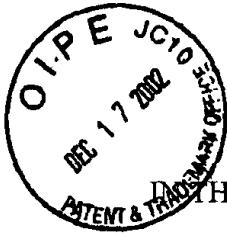
I certify that this document and fee is being deposited on Dec (2) 2002 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

  
Signature of Person Mailing Correspondence

Jeff Rothenberg

Typed or Printed Name of Person Mailing Correspondence

cc:



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Applicant: Grutzendieck et al. Confirmation No.: 4906  
Serial No.: 09/806,224 Examiner: Suk San Feong  
Filed: 06/11/2001 Group Art Unit: 2823  
Title: Method for Producing Transistors

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on December 12, 2002.

Jeff Rosenberg  
Attorney for Applicant  
Reg. No. 26,429

Date of Signature: 12/12/02

Commissioner for Patents  
Washington, D.C. 20231

AMENDMENT

Dear Sir:

In response to the Office Action dated August 14, 2002, kindly amend the above-identified application as follows:

In the Claims:

Rewrite claims 1, 6 and 14 to read as follows:

1. (Twice Amended) A method for producing integrable semiconductor components, in particular transistors, diodes, and logic gates, starting with a p-doped or n-doped semiconductor substrate in the following steps:

12/19/2002 DEMMAH1 00000097 09806224

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